FIG 1

FIG 1A

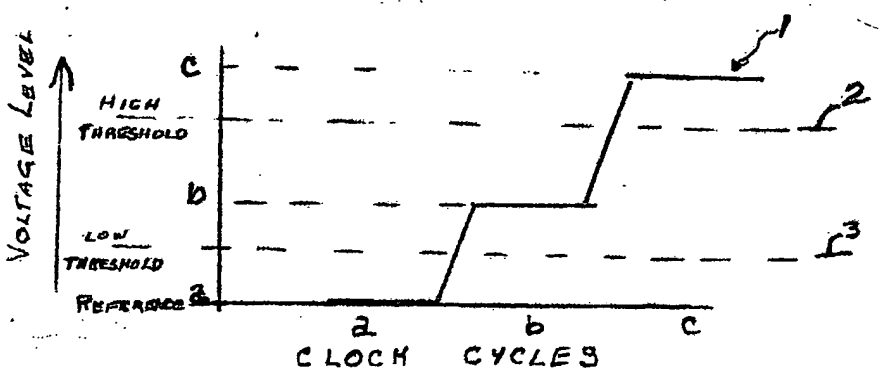
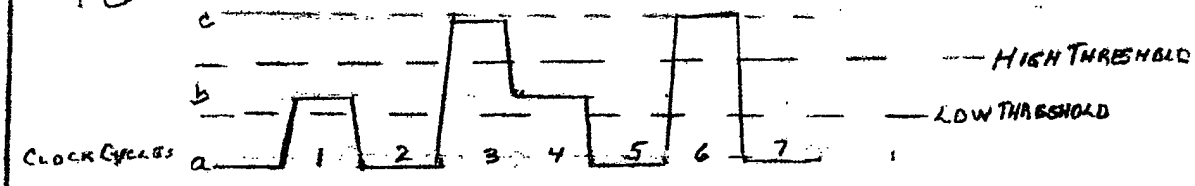


FIG 1B

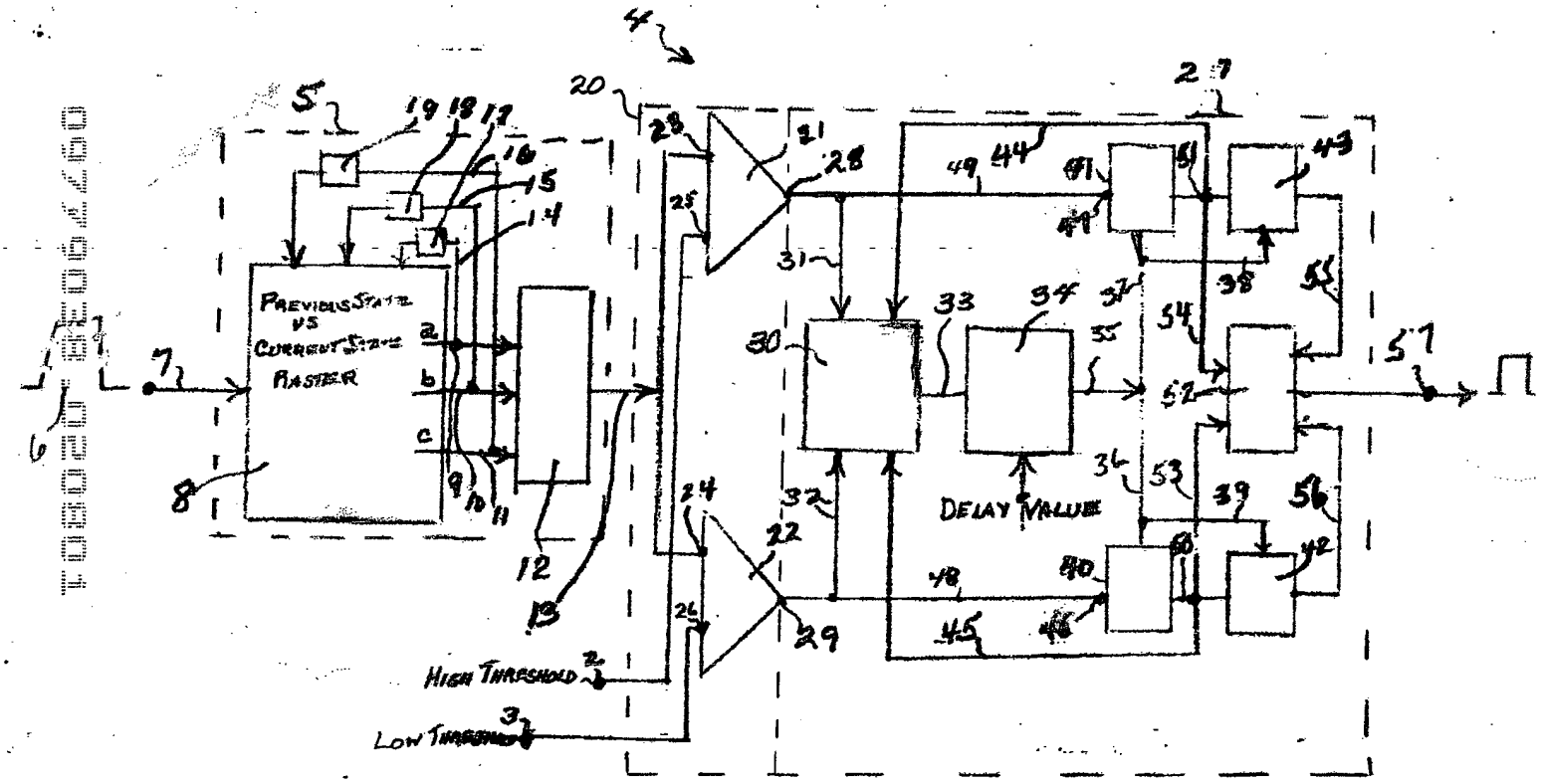
PREVIOUS STATE	CURRENT STATES a	b	c
a	x	0	1
b	0	x	1
c	0	1	x

1C



TRUTH TABLE	b	a	c	b	a	c	a
BINARY DIGIT	0	0	1	1	0	1	0

FIG. 2


PREVIOUS STATE VS CURRENT STATE RASTER
DELAY VALUE
HIGH THRESHOLD
LOW THRESHOLD